FACULTY PROFILE

Name of the Faculty	**JAGADEESH M**
Designation	**Assistant Professor**
Contact Numbers	**Office: 08257- 231141** **Mobile: +91 - 9901730364**
E-mail ID	**jagadeesh.sullia@gmail.com**

• Educational Qualification

Degree	Specialization	Year of Passing	College & University
B.E	Electronics and Communication	2008	KVGCE, Sullia, VTU
M. Tech	VLSI Design and Embedded Systems	2014	KVGCE, Sullia, VTU

• Work Experience

Teaching	Research	Industry
14+Experience	-	**6 Months**

• Date of Joining to the College

12th February 2009

• In-House Experience

Designation	Duration		Department
	From	**To**	
Lecturer	12-02-2009	31-07-2014	E & C
Asst. Professor	01-08-2014	Till the Date	E & C

• Outside Experience

Designation	Place	Duration	
		From	**To**
QC Engineer	Smile Electronics, Bangalore	01-08-2008	10-02-2009

• Area of Interest

Embedded System	Assembly and Embedded C Programming	Signal Processing	Electrical Network Analysis	Electromagnetic Field Theory	Communication Systems

• Subject Taught

1. Antennas and Wave Propagation
2. Analog communication
3. Signals And Systems
4. Field Theory
5. Satellite Communication
6. Network Analysis
7. Optical Fiber Communication
8. Basic Signal Processing
9. 8051 Microcontroller
10. Electromagnetic Field Theory
11. Digital Switching System
12. Analog and Mixed Mode VLSI Design
13. Error Control Coding
14. Control System
15. ARM microcontroller and embedded systems
16. Data structure using C++

• Publications

National / International Journals	National / International Conferences
01	**01**

• Publications : National / International Conferences/Journals

1. Design And Analysis of An Area Efficient Boostable Repeater for Variation Resilience in VLSI Interconnect, "*6th National Conference on Recent Trends in Electronics and Communication Engineering*" at SJBIT, Bangalore, 10th May 2014.

2. "Machine Learning Based Genetic Algorithmic Optimization Model for Inpatient Length-of-Stay Prediction" has been published in International Journal of Engineering Research and Applications (IJERA). ISSN: 2248-9622, Vol No.: 13, Issue No.: 03, March-2023

• Professional Activities: NPTEL Course

NPTEL (National Program on Technology Enhanced Learning)	Subject	Year of Certification	Result
	1. Teaching and Learning in Engineering (TALE) (Prof. N J Rao, IISc, Bangalore)	March-2019 (4weeks. FDP)	Pass(55%)

• Faculty Development Programmes (FDP) Participated

SL. No.	Program Title	Venue	Date & Year
1.	Faculty Enrichment Program	VTU Regional Office, Mysore	23-07-2018 to 27-07--2018 (Five days)
2.	Programming fundamentals using Python	Infosys Spring Board online course	17-10-2022 to 19-10-2022 (Three days)
3.	Faculty Development Program- Python- A practical approach	KVGCE Sullia	13-06-2023 to 14-06-2023 (Two days)

• No. of Project Guided

UG	PG
20	1

• Responsibilities Taken in College Level

Event	Positions	Duration	
		From	From
SC/ST Grievance Cell	Program coordinator	2012	12-06-2022
NBA	Coordinator for Criteria – 8	2018	2021
NBA	Coordinator for Criteria – 4	2018	Till date
SC/ST /OBC Cell	Chief Coordinator	13-06-2022	Till date

Event	Role	Duration	
		From	To
1. Time Table	Coordinator	2011	Till date
2. 8051 Microcontroller Lab	In-charge	2014	2015
3. DIMENSION	Coordinator	2017	Till date
4. Internal Test	Coordinator	2018	Till date
5.University Examination	Coordinator	2020	Till date
6.Embedded System Lab	In-charge	2020	Till date
7.Question Paper Scrutiny Committee	Member -secretary	2021	Till date
8.PAC	Member	2021	Till date
9. IOT and ML Club	Coordinator	2022	Till date
10. Question Paper Scrutiny Committee	Member	2021	Till date
11. FDP on Ability Enhancement Course-Basics of C++	Coordinator & Resource Person	2023	-

• Responsibilities Taken in Department Level

- **Participation details in the college level event.**

SL .No.	Event	Position / Role
1.	College sports meet	Volunteers committee
2.	Alumni meet	Program committee
3.	College Day function	Member
4.	College Day function	Member - Food committee
5.	Science Talent Hunt Program	Member for Quiz Committee
6.	Shilpi Magazine-XXV	Member

- **Participation details in the Department level activities**

SL .No.	Event	Role / Responsibilities
1.	e-congregation-2012	Coordinator
2.	e-congregation -2013 to till date	Member
3.	Counseling	Counselor for EC Students

- **No. of Lab Manual / Program Guidelines Prepared**

SL. No.	Lab manual / Guide lines Title	Semester and branch	Year
1.	Microcontroller Lab Manual	IV E&C	2014
2.	Embedded System Lab Manual	VI E&C	2021
3.	FDP Manual for 'Ability Enhancement Course – Basics of C++	FDP	2023

- **No. of Labs handled for UG and PG**

SL. No.	Lab Title	Year	Semester & Branch
1.	Microcontroller lab	2009 to 2019	IV E&C & V E&E
2.	HDL Lab	2009	IV E&C
3.	Analog Electronics Lab	2010	III E&C
4.	Logic Design Lab	2010	III E&C
5.	DSP LAB	2010 to 2015	V E&C

6.	Power Electronics Lab	2012	VII EC
7.	VLSI Lab	2011	VII EC
8.	Embedded Controller lab	2018	VI EC
9.	C++	2023	II SEM

• Interaction with Outside World / Invited Lectures

Sl.No.	Program	Venue and Organizer	Date
1.	Judge for Project Exhibition	Srinivasa college of Engineering and Technology, Srinivasa University, Mukka, Mangaluru.	22.05.2019
2.	Technical Talk, Resource person. (Advanced Microprocessor)	KVGP, Sullia	10.04.2018
	Technical Talk, Resource person. (Basics of Python)	KVGP, Sullia	19-05-2023

• Awards / Recognition / Achievements/ Others

- Served as **Question Paper setter** for UG Program (Signals and Systems) for NMAMIT @ Nitte
- Participated in State Level Sullia half marathon on 19-01-2019
- Served as **Question Paper setter** for PG Program (Real Time Systems) for V T U

PROF. JAGADEESH M, E &C DEPT., KVGCE, SULLIA, D.K - 574 327

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