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REPORT OF “PARAM Uthkarsh supercomputing workshop”

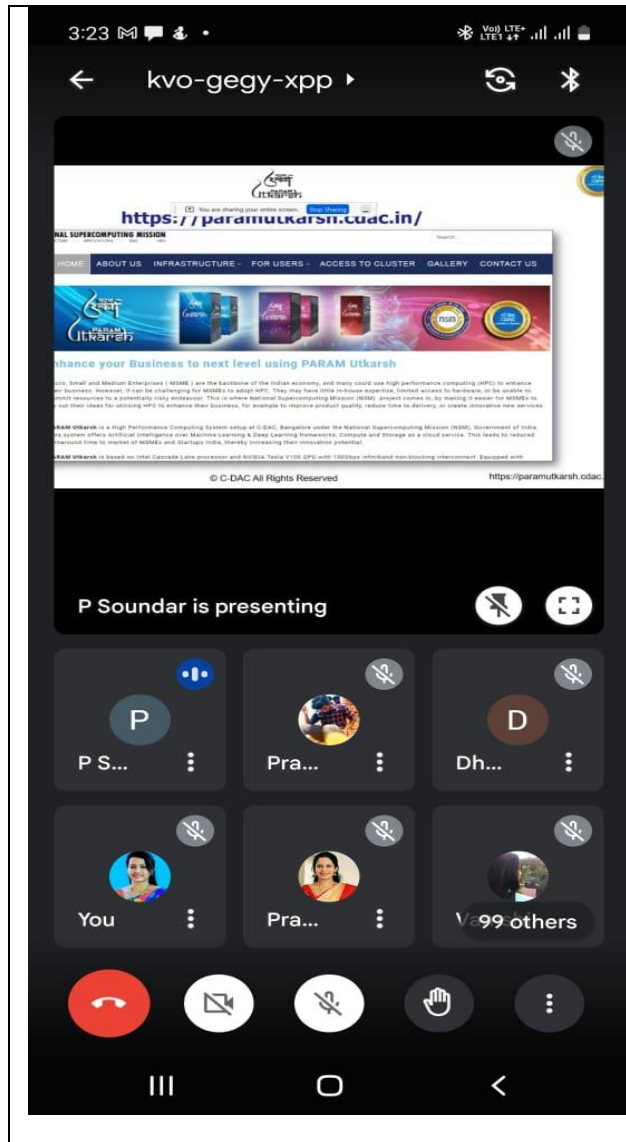
A program on “PARAM Uthkarsh supercomputing workshop” was organized by KVGCE R&D Cell in virtual mode (Google Meet link: <https://meet.google.com/kvo-gegy-xpp>) on 15th December 2022. The Centre for Development of Advanced Computing (C-DAC), Bangalore, a scientific society under Ministry of electronics and information technology, Government of India has provided information on the supercomputing facility through Ms. Divya M G, Associate Director of CPSF Group with an objective to establish multiple Super computers across the country along with research towards digitization

Information on HPC and Top Supercomputers, PARAM Utkarsh Architecture was presented by Mr. Soundararajan P A, Joint Director, CPSF group, CDAC Bangalore. Then details of Software Stack , its Applications & Libraries of HPC Scheduler- SLURM, demonstration of a job submission was provided. Finally, information on successful ML/DL and HPC applications was shared. Dr.Savitha C. K, Dean-Research and Professor of CS&E department gave the opening remarks. Program attendees included the CEO Dr.Ujwal U.J, Principal Dr. Suresha V., and heads of all departments, faculty members, research supervisors, research scholars, PG and final year UG students from various branches.



➤ **PARAM Siddhi - AI of 5.26 Petaflops (210 AI Petaflops) is the fastest Supercomputer in India and ranked at No. 62 position in 'TOP500 Supercomputer List – November 2020' declared at Supercomputing Conference 2020 (SC 20) at United States.**

➤ **HPC-AI Convergence**



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